

Fig. 1

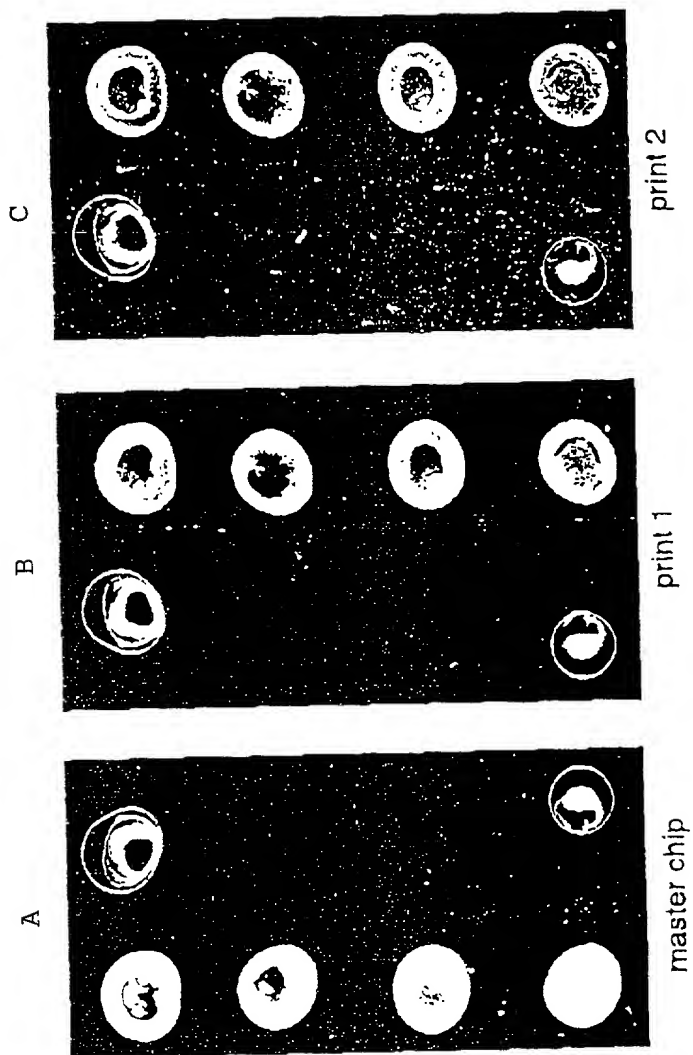


Fig. 2

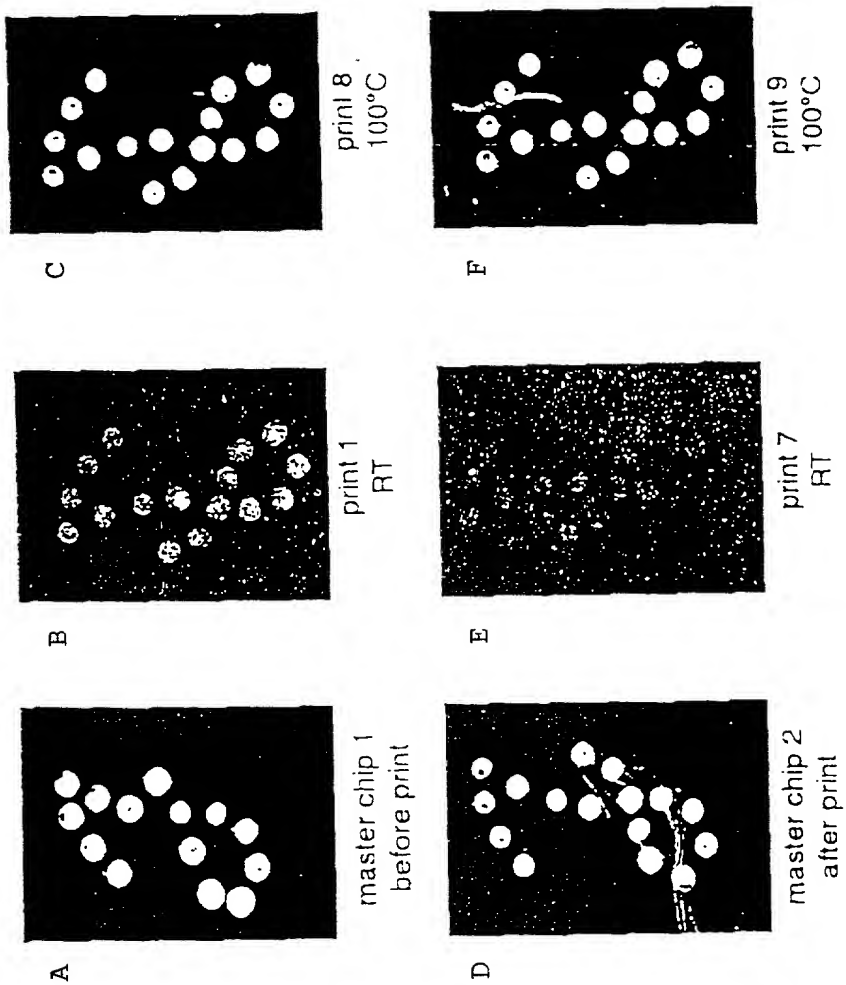


Fig. 3

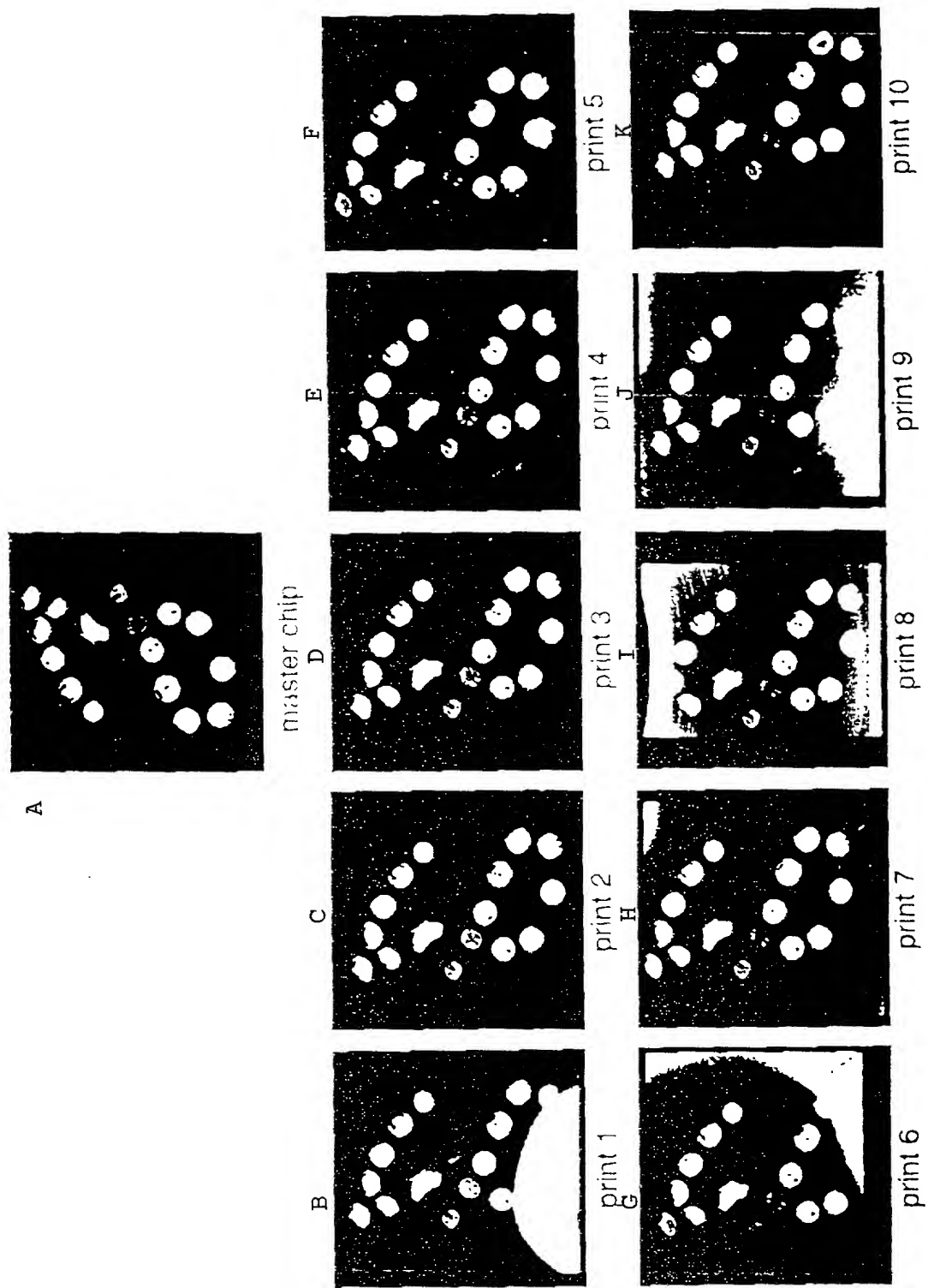
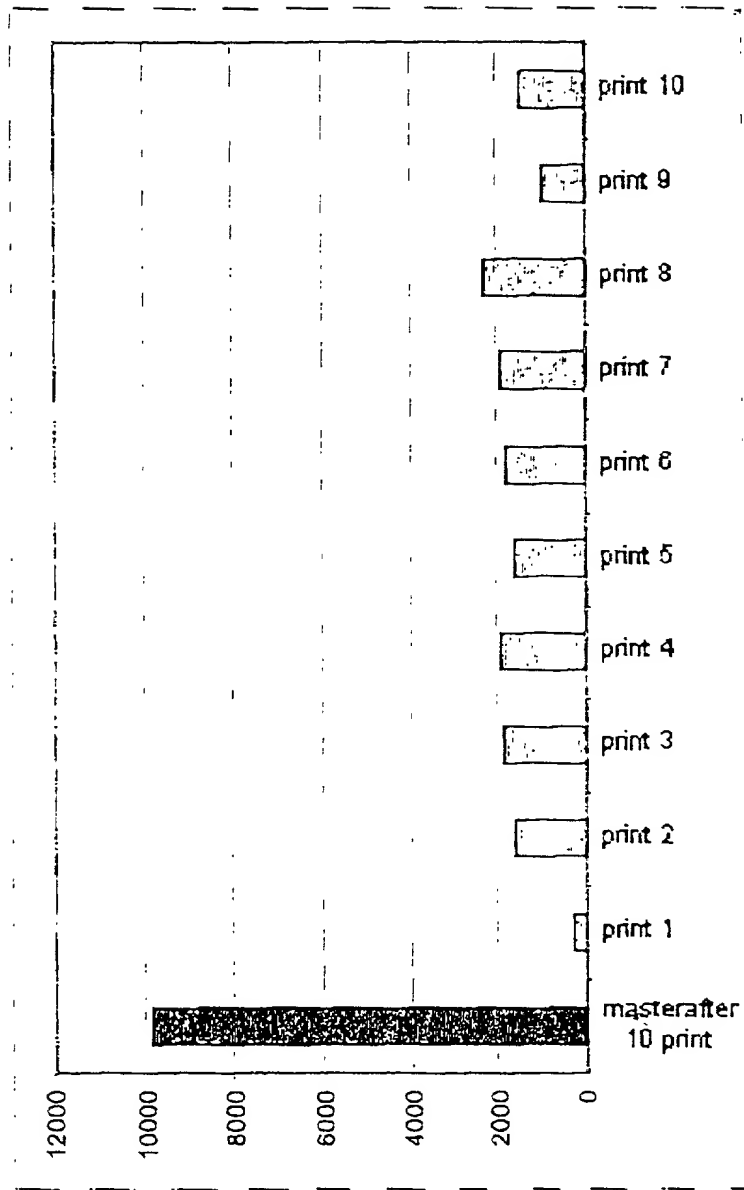


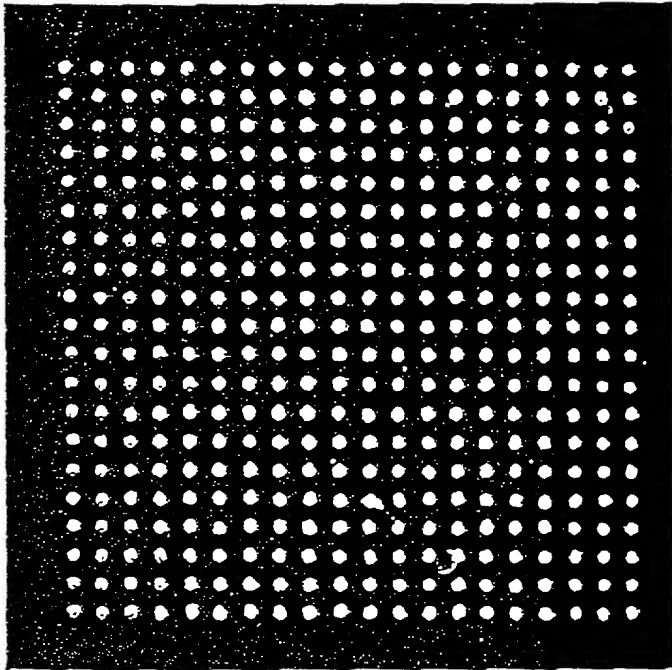
Fig. 4



FOOTNOTES

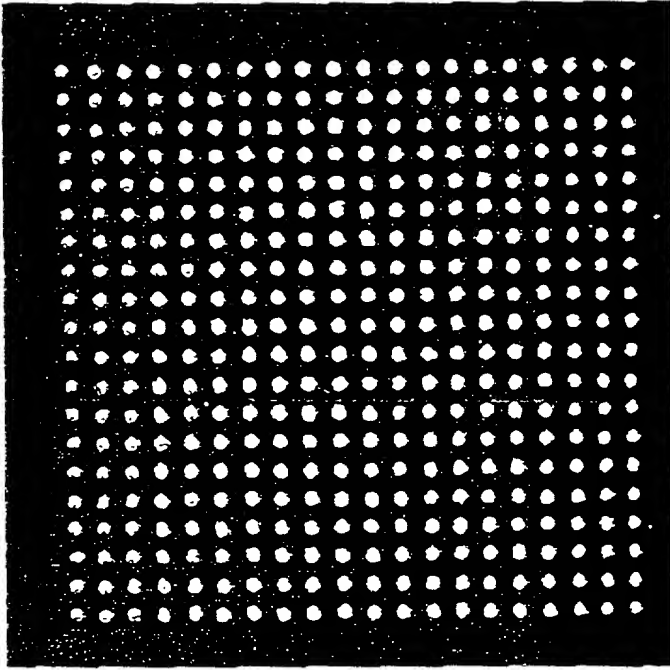
Fig. 5

A



master chip

B



print chip

Fig. 6

